

Time	January 2	Time	January 3	January 4	January 5	January 6
8:30-9:15	Registration					
9:15-10	Inauguration	9-11:15	Amplifier Design Considerations	Oscillator Design Considerations	Layout Design and EM Simulation	Measurement session
10-10:30	Tea break	11-15-11:45	Tea break	Tea break	Tea break	Tea break
10:30-12:30	Keynote Dr. Kushal Tuckley	11:45-12:45	Shri. G. R. Shinde Astra Microwaves	Dr. Sandip Chaturvedi, GAETEC	Layout Design and EM Simulation	Valedictorian session
12:30-1:30	Lunch	12:45-1:45	Lunch	Lunch	Lunch	
1:30-3:45	Basic Concepts in active circuit design Dr. Zinka	1:45-3:45	LNA Design	Hands on Oscillator Design	Visit to Astra Microwaves	
3:45-4:15	Tea break	3:45-4:15	Tea break	Tea break	Visit to Astra Microwaves	
4:15-6 pm	Introduction to AWR Manjunath Reddy	4:15-6:00	Hands on-Power Amplifier Design	Hands on-Oscillator Design	Visit to Astra Microwaves	

Workshop Committees	
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A Workshop

On

Practical RF/ Microwave Active Circuit Design

From

January 2-6, 2019

Organized by



Department of
Electrical & Electronics Engineering
BITS Pilani, Hyderabad Campus

and

NI AWR



About BITS

Birla Institute of Technology & Science Pilani (BITS-Pilani) is an institution of international repute. Over the years, BITS-Pilani has provided the highest quality technical education to students from all over India and abroad admitted on the basis of merit. BITS-Pilani is ranked among the top engineering institutes of India. BITS is a multi-locational university with three campuses in India, at Pilani, Goa and Hyderabad, and one international campus in Dubai (UAE). During its five decades of existence as a university, BITS-Pilani has established strong linkages with industries, R&D organizations and financial institutions through its university-industry linkage programmes.

About the EEE Department

The Department of Electrical & Electronics Engineering at BITS-Pilani, Hyderabad campus started right at the inception of the campus in 2008 and has been offering B. E. (Hons) Electrical and Electronics Engineering, B. E. (Hons) Electrical and Communication Engineering, B. E. (Hons) Electronics and Instrumentation Engineering, M. E. (Communication Engineering), M. E. (Microelectronics Engineering) and Doctoral Programs. The students of all tiers have access to both expert faculty members in the department and institute supported industry engagement opportunities. The department has extensive laboratory and infrastructural facilities for teaching, training, research and development.

How to reach

The campus is located at about 60 km from Rajiv Gandhi International Airport Shamshabad and about 25 km from Secunderabad railway station. From Secunderabad railway station (Gurudwara Point), there is a direct Bus No. 212 to BITS Pilani Hyderabad Campus main gate for every one hour. Besides participants can take the bus number 211S, 211A, 211C, 211D, 211E, 211J, 211K, 211T, 211U, 567, 568, 569 and get down at Thumkuntha village to take an auto-rickshaw to reach BITS Pilani Hyderabad campus.

About the course

The objective of this hands-on training course will be to provide training on a practical approach for the design, simulation and optimization techniques for RF/Microwave active circuits. The course will cover the relevant theoretical aspects along with emphasis on the practical considerations of RF/Microwave engineering. The course is structured on live demonstrations involving computer-based simulation tools to illustrate concepts. Participants will be trained on the usage of NI/AWR Microwave Office design suite and RF measurement techniques. The course faculties are domain experts with many years of experience from reputed industries and academia. The aim of the course is to enable participants to acquire the skill sets on the practical design and simulation of RF / Microwave active circuits.

Resource Persons

Dr. Kushal Tuckley, Adjunct Faculty, IIT-Bombay
Shri. G. R. Shinde, General Manager, Astra Microwaves
Dr. Sandip Chaturvedi, GAETEC, Hyderabad
Dr. Srinivas Zinka, HBL, Hyderabad
Mr. Manjunath Reddy, NI, Sr. Member, IEEE
Dr. Harish V. Dixit, Asst. Professor, BITS-Pilani, Hyderabad

Course Objectives

To impart

- sound knowledge on the performance parameters, characterization RF/ Microwave Active Circuits
- practical skill sets on the usage of NI/Microwave office EDA tool
- hands on practical design & optimize skill sets on Low Noise Amplifiers (LNA), Power Amplifiers (PA)
- hands on practical training design, simulate and optimize Oscillators & VCOs

Who can attend

UG/PG Students, Faculties and Practicing professionals with basic knowledge on RF Circuits, Wireless Communication.

Registration Fees

Research Scholars and Students	Rs. 4000/-
Attendees from Academia/Industry	Rs. 6000/-

Registration fees would include access to all the sessions, industrial visit, course kit/course material, tea/snacks and working lunch.

Accommodation

Limited shared accommodation on nominal charges are available in the student hostel on first come first serve basis. A few number of hotels are available in the vicinity of the campus for interested participants. The details would be uploaded on website.

Webpage

<http://universe.bits-pilani.ac.in/hyderabad/macd/macd>

Registration

Participants can pre-register online via google form at the following link and send the payment information to the convenor once the bank details are made available on the website.

tinurl.com/macd2019

Bank account details would be made available on the website by **December 8, 2018**

Important Dates

Registration begins: **November 30, 2018**

Registration Ends: **December 15, 2018**

Confirmation of registration by organizer: **By December 16, 2018**

Contact

Email: hvdixit@hyderabad.bits-pilani.ac.in

Phone:

Dr. Harish V. Dixit: 9049 11 88 77

Dr. Runa Kumari: 741 681 72 76